

sealant in the sealing region, the sealant attaching the first substrate and the second  
substrate to one another and maintaining a gap therebetween;

a light-shielding layer on the second substrate except regions corresponding to the thin  
film transistors and pixel electrodes, and a region where the sealant attaches to the second  
substrate; and

a liquid crystal layer within the gap and on the active region side of the sealant.

Kindly add new claims 18-20.

--18. A liquid crystal display (LCD) panel comprising:

first and second substrates;

an active region on the first and second substrates, the active region including a plurality  
of thin film transistors and pixel electrodes;

a pad region along a periphery of the active region and along a corresponding region of  
the second substrate;

sealant in a sealing region in the pad region, the sealant attaching the first substrate and  
the second substrate to one another and maintaining a gap therebetween;

a light-shielding layer in the pad region and the active region on the second substrate,  
wherein the light-shielding layer substantially covers the entire pad region except where the  
sealant attaches to the second substrate; and

a liquid crystal layer within the gap and on the active region.

19. The LCD panel of claim 18, wherein the sealant is an UV-type hardening sealant.

20. The LCD panel of claim 18, wherein the light-shielding layer has a matrix arrangement in the active region.--

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